Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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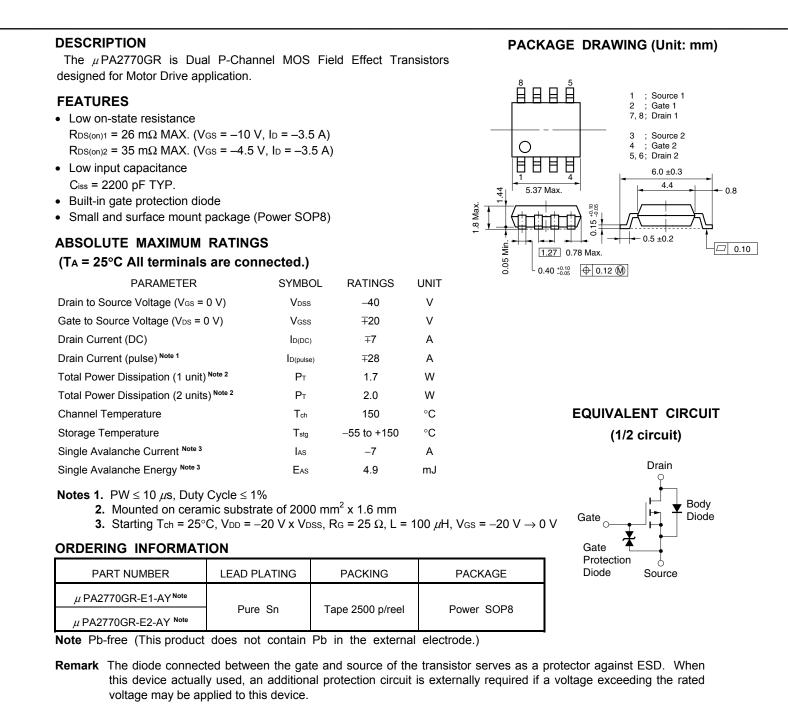
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Renesas

MOS FIELD EFFECT TRANSISTOR $\mu PA2770GR$

SWITCHING DUAL P-CHANNEL POWER MOS FET



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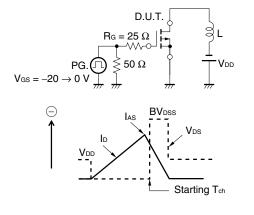
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -40 V, V _{GS} = 0 V			-10	μA
Gate Leakage Current	lgss	V _{GS} = ∓20 V, V _{DS} = 0 V			∓10	μA
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-1.0	-1.6	-2.5	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = -10 V, I _D = -3.5 A	5	11		S
Drain to Source On-state Resistance Note	RDS(on)1	V_{GS} = -10 V, I _D = -3.5 A		21	26	mΩ
Drain to Source On-state Resistance Note	RDS(on)2	V_{GS} = -4.5 V, I _D = -3.5 A		24	35	mΩ
Input Capacitance	Ciss	V _{DS} = -10 V		2200		pF
Output Capacitance	Coss	V _{GS} = 0 V		350		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		260		pF
Turn-on Delay Time	td(on)	V _{DD} = -20 V, I _D = -3.5 A		11		ns
Rise Time	tr	V _{GS} = -10 V		27		ns
Turn-off Delay Time	$t_{d(off)}$	R _G = 10 Ω		160		ns
Fall Time	tr			88		ns
Total Gate Charge	Q _G	I _D = -7 A		45		nC
Gate to Source Charge	Q _{GS}	V _{DD} = -32 V		5.2		nC
Gate to Drain Charge	Qgd	V _{GS} = -10 V		12		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 7 A, VGS = 0 V		0.84	1.5	V
Reverse Recovery Time	trr	IF = -7 A, VGS = 0 V		54		ns
Reverse Recovery Charge	Qrr	di/dt = -50 A/ <i>µ</i> s		25		nC

ELECTRICAL CHARACTERISTICS (TA = 25°C, All terminals are connected.)

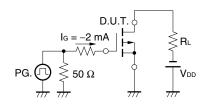
Note Pulsed

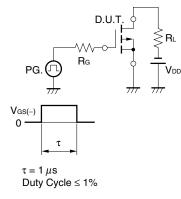
TEST CIRCUIT 1 AVALANCHE CAPABILITY

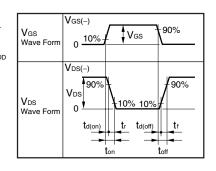
TEST CIRCUIT 2 SWITCHING TIME



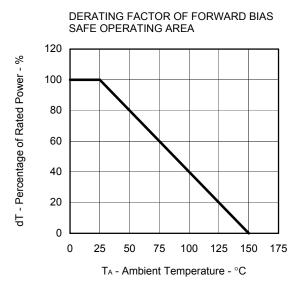
TEST CIRCUIT 3 GATE CHARGE



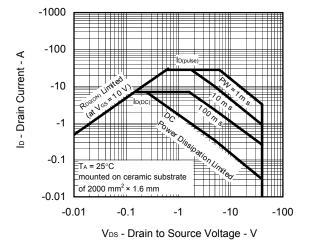


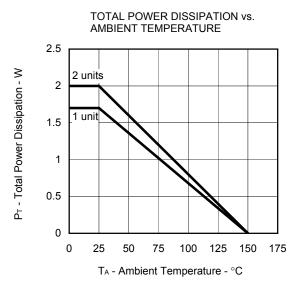


TYPICAL CHARACTERISTICS (T_A = 25°C)

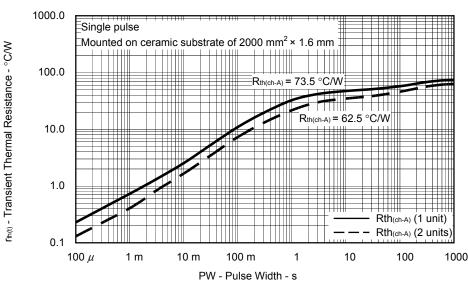


FORWARD BIAS SAFE OPERATING AREA

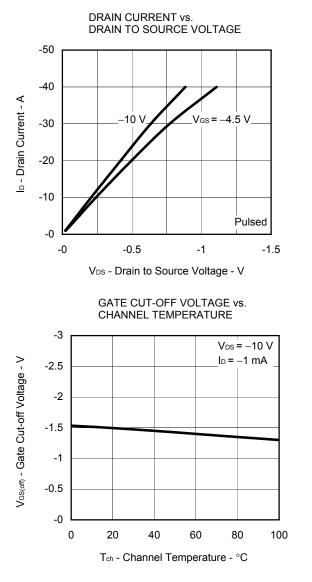


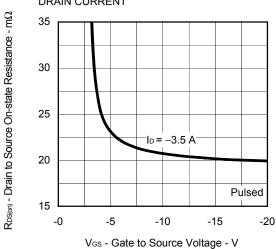


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



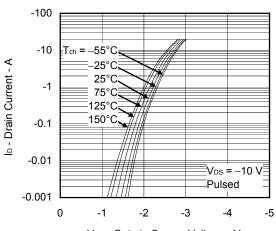
Data Sheet G19864EJ1V0DS





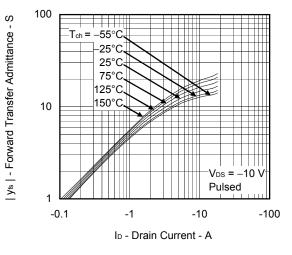
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

FORWARD TRANSFER CHARACTERISTICS

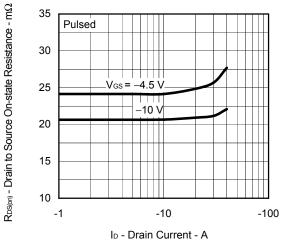


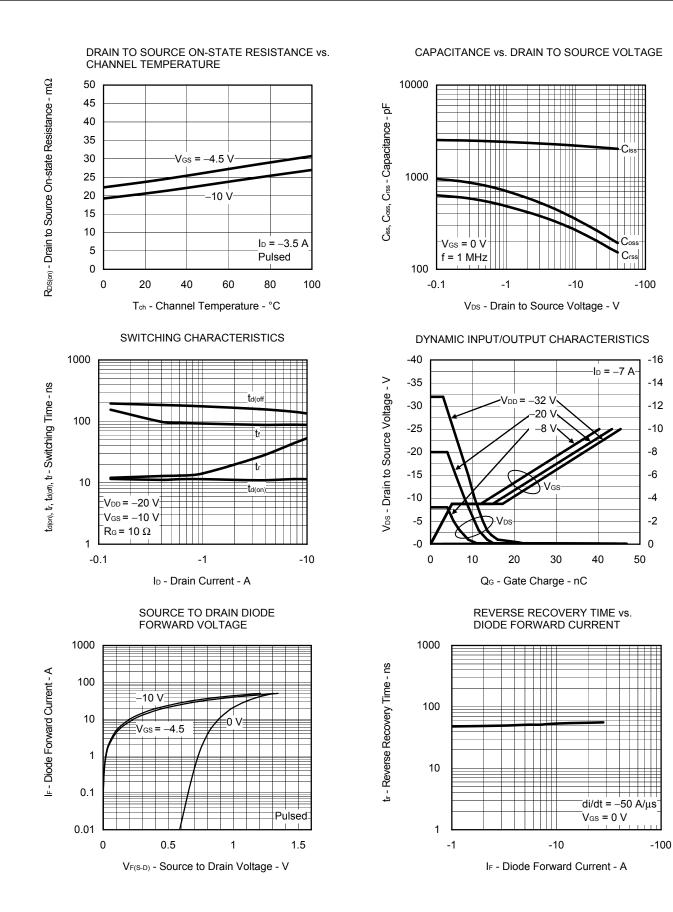
VGS - Gate to Source Voltage - V

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

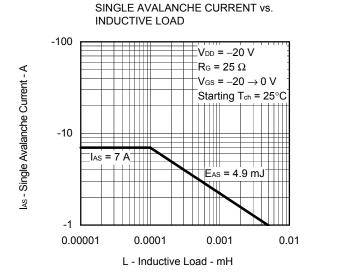


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

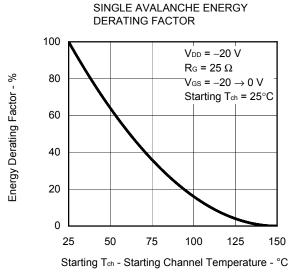




V_{GS} - Gate to Source Voltage - V



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