

RQJ0303PGDQA

Silicon P Channel MOS FET **Power Switching**

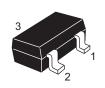
R07DS0295EJ0600 Rev.6.00 Jan 10, 2014

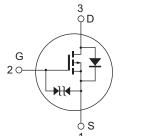
Features

- Low on-resistance
 - $R_{DS(on)} = 54 \text{ m}\Omega \text{ typ } (V_{GS} = -10 \text{ V}, I_D = -1.6 \text{ A})$
- Low drive current
- High speed switching
- 4.5 V gate drive

Outline

RENESAS Package code: PLSP0003ZB-A (Package name: MPAK)





1. Source

2. Gate

3. Drain

Note: Marking is "PG".

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	-30	V
Gate to source voltage	V_{GSS}	+10 / –20	V
Drain current	I _D	-3.3	А
Drain peak current	I _{D(Pulse)} Note1	-5	A
Body - drain diode reverse drain current	I _{DR}	-3.3	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: $40 \times 40 \times 1$ mm)

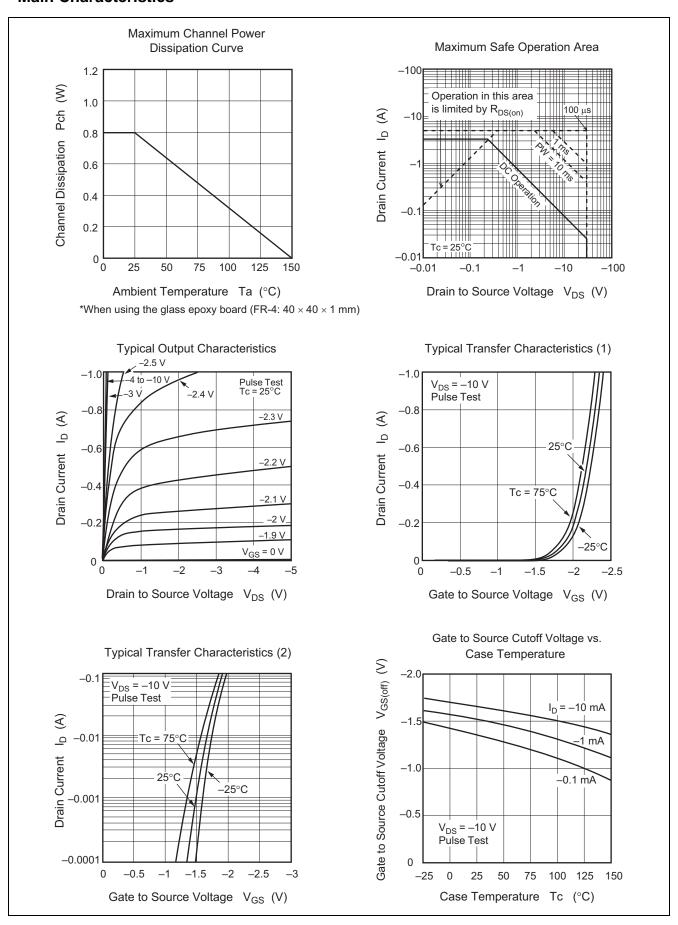
Electrical Characteristics

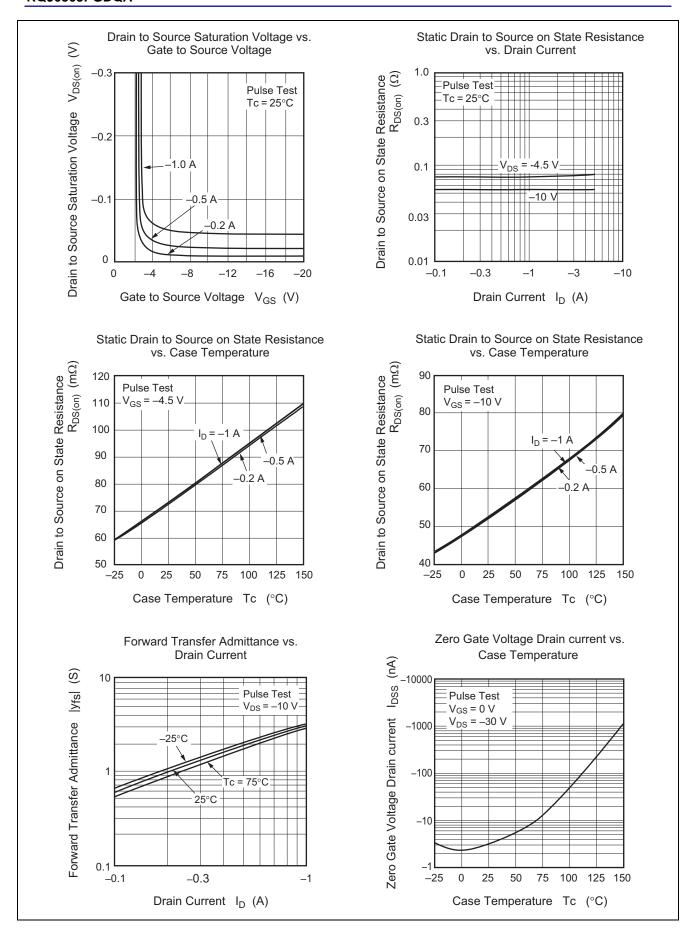
 $(Ta = 25^{\circ}C)$

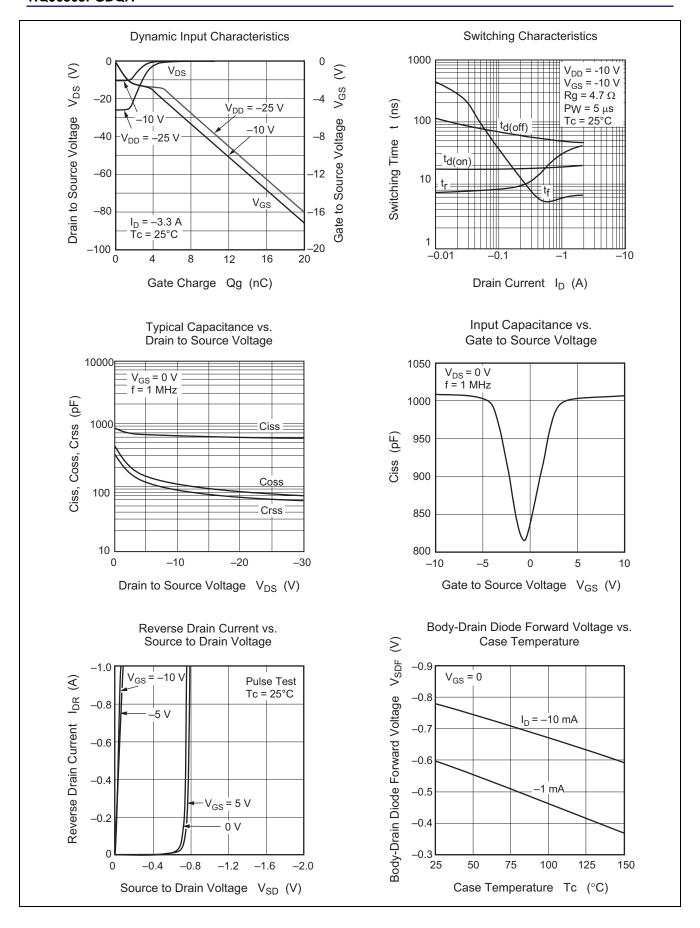
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+10	_	_	V	$I_G = +100 \mu A, V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	-20	_	_	V	$I_G = -100 \mu\text{A}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	+10	μΑ	$V_{GS} = +8 \text{ V}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	-10	μΑ	$V_{GS} = -16 \text{ V}, V_{DS} = 0$
Drain to source leak current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	_	54	68	mΩ	$I_D = -1.6 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note3}}$
	R _{DS(on)}	_	76	107	mΩ	$I_D = -1.6 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	2.5	4.2	_	S	$I_D = -1.6 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	625	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	111	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	83	_	pF	
Turn - on delay time	t _{d(on)}	_	18	_	ns	$I_D = -1 A, V_{GS} = -10 V,$
Rise time	t _r	_	29	_	ns	$R_L = 6.6 \Omega$, $Rg = 4.7 \Omega$
Turn - off delay time	$t_{d(off)}$	_	47	_	ns	
Fall time	t _f	_	5.7	_	ns	
Total gate charge	Qg	_	12	_	nC	$V_{DD} = -10 \text{ V}, V_{GS} = -10 \text{ V},$
Gate to source charge	Qgs	_	1.5	_	nC	$I_D = -3.3A$
Gate to drain charge	Qgd	_	2.9	_	nC	
Body - drain diode forward voltage	V_{DF}	_	-0.9	_	V	$I_F = -1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

Main Characteristics

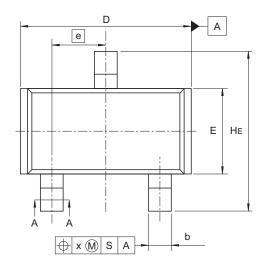


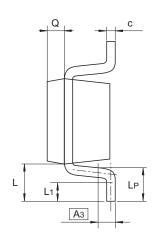


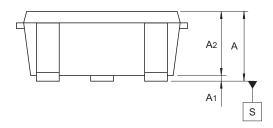


Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-59A	PLSP0003ZB-A	MPAK(T) / MPAK(T)V	0.011









Reference	Dimensions in millimeters		
Symbol	Min	Nom	Max
Α	1.0	_	1.3
A ₁	0	_	0.1
A ₂	1.0	1.1	1.2
A ₃		0.25	_
b	0.35	0.4	0.5
С	0.1	0.16	0.26
D	2.7	_	3.1
E	1.35	1.5	1.65
е		0.95	
HE	2.2	2.8	3.0
L	0.35	_	0.75
L ₁	0.15		0.55
Lp	0.25	_	0.65
Х			0.05
Q	_	0.3	_

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Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQJ0303PGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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